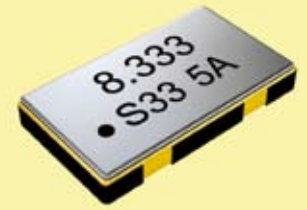


OSC61

CLOCK OSCILLATOR

SMD



TYPICAL APPLICATIONS

Palmtop Computer, Portable Application, PCMCIA Cards Disc Drives

FEATURES

Ultra-thin, Ceramic SMD Package, Lead-free Design, Seam Sealed, High Performance, CMOS & TTL Compatible, RoHS Compliant Standard

DIMENSION

6.0 x 3.5 x 1.8[mm]



TYPICAL SPECIFICATIONS

Model		OSC61	
Nominal Frequency Range		0.5 ~107MHz	0.5 ~125MHz
Standard Frequency		20, 27, 40, 44, 50, 66, 106.25, 125MHz	
Frequency Stability		±25 ppm, ±50 ppm, ±100 ppm	
Aging (+25°C)		±5 ppm / year Max.	
Temperature Range	Operating	0°C ~ +70°C	
	Storage	-55°C ~ +125°C	
Power Supply Voltage(VDC)		+5V(±10%)	+3.3V
Current Consumption		10(*15)mA Max.: 0.5~14.9MHz 15(*20)mA Max.: 15~29.9MHz 25(*30)mA Max.: 30~39.9MHz 35(*40)mA Max.: 40~49.9MHz 40(*50)mA Max.: 50~59.9MHz 40(*60)mA Max.: 60~69.9MHz 50(*70)mA Max.: 70~89.9MHz 60(*80)mA Max.: 90~107MHz	10(*10)mA Max.: 0.5~14.9MHz 15(*15)mA Max.: 15~29.9MHz 20(*25)mA Max.: 30~39.9MHz 25(*30)mA Max.: 40~49.9MHz 30(*35)mA Max.: 50~59.9MHz 35(*45)mA Max.: 60~79.9MHz 45(*60)mA Max.: 80~99.9MHz 50(*70)mA Max.: 100~125MHz

Output TTL-CMOS(See Table A)

TABLE A

Output	Load	15pF(*50pF Max.)	2TTL or 5TTL or 10TTL
	Level	CMOS	TTL
	Symmetry	45% ~ 55% @ 50%Vcc F ≤ 50MHz 40% ~ 60% @ 50%Vcc F > 50MHz	40% ~ 60% @ 1.4VDC
	Rise & Fall Time	10ns Max. F ≤ 40MHz 6ns Max. F > 40MHz	
		@10%~90% Vcc	@0.4~2.4 VDC
Voltage	"0" level	Vol : 0.1Vcc Max.	Vol : 0.4 Vcc Max.
	"1" level	Voh : 0.9Vcc Min.	Voh : 2.4Vcc Min.

Period Jitter RMS

5ps Max

Phase Jitter RMS

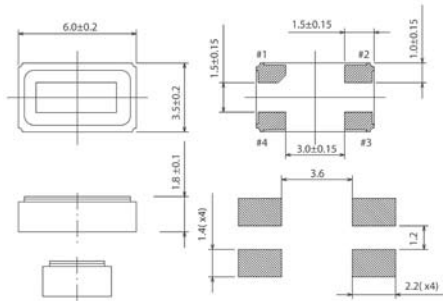
1ps Max. Fj=12k~20MHz

Please consult with our sales department for other parameters.

* Load: 50pF

Package quantity 1,000 pcs or 3,000 pcs max./reel.

OUTLINE DIMENSIONS
Unit:mm



PIN NO	CONNECTION
# 1	N.C.(INH)
# 2	GND
# 3	OUTPUT
# 4	+Vcc

All specifications are subject to change without notice.