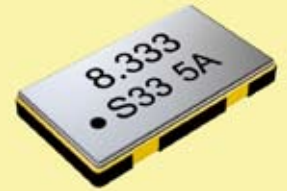


# OSC71

CLOCK OSCILLATOR

SMD



TYPICAL APPLICATIONS

Palmtop Computer, Portable Application, PCMCIA Cards Disc Drives

FEATURES

Ultra-thin, Ceramic SMD Package, Seam Sealed, Lead-free Design, High Performance, CMOS & TTL Compatible, RoHS Compliant Standard

DIMENSION

5.0x3.2x1.0[mm]



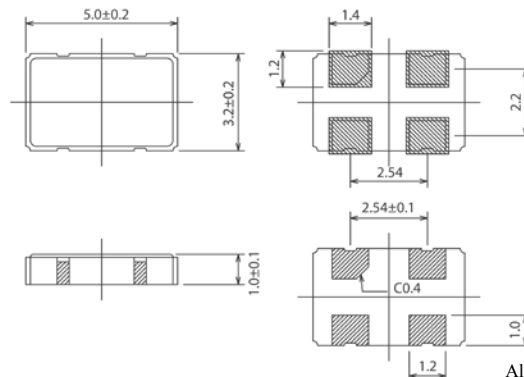
TYPICAL SPECIFICATIONS

Model		OSC71			
Nominal Frequency Range		0.5 ~70MHz		0.5 ~125MHz	
Standard Frequency		20, 27, 40, 44, 48, 50, 66, 75MHz			
Frequency Stability		±25 ppm, ±50 ppm, ±100 ppm			
Aging (+25°C)		±5 ppm / year Max.			
Temperature Range	Operating	0°C ~ +70°C, -40°C ~ +85°C			
	Storage	-55°C ~ +125°C			
Power Supply Voltage(VDC)		+5V(±10%)		+3.3V(±10%)	
Current Consumption		10(*15)mA Max.: 0.5~14.9MHz 15(*20)mA Max.: 15~29.9MHz 25(*30)mA Max.: 30~39.9MHz 35(*40)mA Max.: 40~49.9MHz 40(*50)mA Max.: 50~59.9MHz 40(*60)mA Max.: 60~75.0MHz		10(*10)mA Max.: 0.5~14.9MHz 15(*15)mA Max.: 15~29.9MHz 20(*25)mA Max.: 30~39.9MHz 25(*30)mA Max.: 40~49.9MHz 30(*35)mA Max.: 50~59.9MHz 40(*45)mA Max.: 60~79.9MHz 50(*60)mA Max.: 80~99.9MHz 60(*70)mA Max.: 100~125MHz	
Output		TTL-CMOS(See Table A)			
Output	Load	15pF(*30pF Max.)	2TTL or 5TTL or 10TTL	15pF(*30pF Max.)	2TTL or 5TTL or 10TTL
	Level	CMOS	TTL	CMOS	TTL
	Symmetry	40%~60% @ 1/2Vcc	40%~60% @ 1.4VDC	40%~60% @ 1/2Vcc	40%~60% @ 1.4VDC
	Rise & Fall Time	10ns Max. F≤40MHz		10ns Max. F≤40MHz	
		7ns Max. F>40MHz		7ns Max. F>40MHz	
		@10%~90% Vcc	@0.4~2.4 VDC	@10%~90% Vcc	@0.4~2.4 VDC
Voltage	"0" level	Vol : 0.1 Vcc Max.	Vol : 0.4Vcc Max.	Vol : 0.1 Vcc Max.	Vol : 0.4Vcc Max.
	"1" level	Voh : 0.9 Vcc Min.	Voh : 2.4Vcc Min.	Voh : 0.9 Vcc Min.	Voh : 2.4Vcc Min.
Period Jitter RMS		5ps Max			
Phase Jitter RMS		1ps Max. Fj=12k~20MHz			

TABLE A

OUTLINE DIMENSIONS

Unit:mm



PIN NO	CONNECTION
# 1	N.C.( INH )
# 2	GND
# 3	OUTPUT
# 4	+Vcc

All specifications are subject to change without notice.